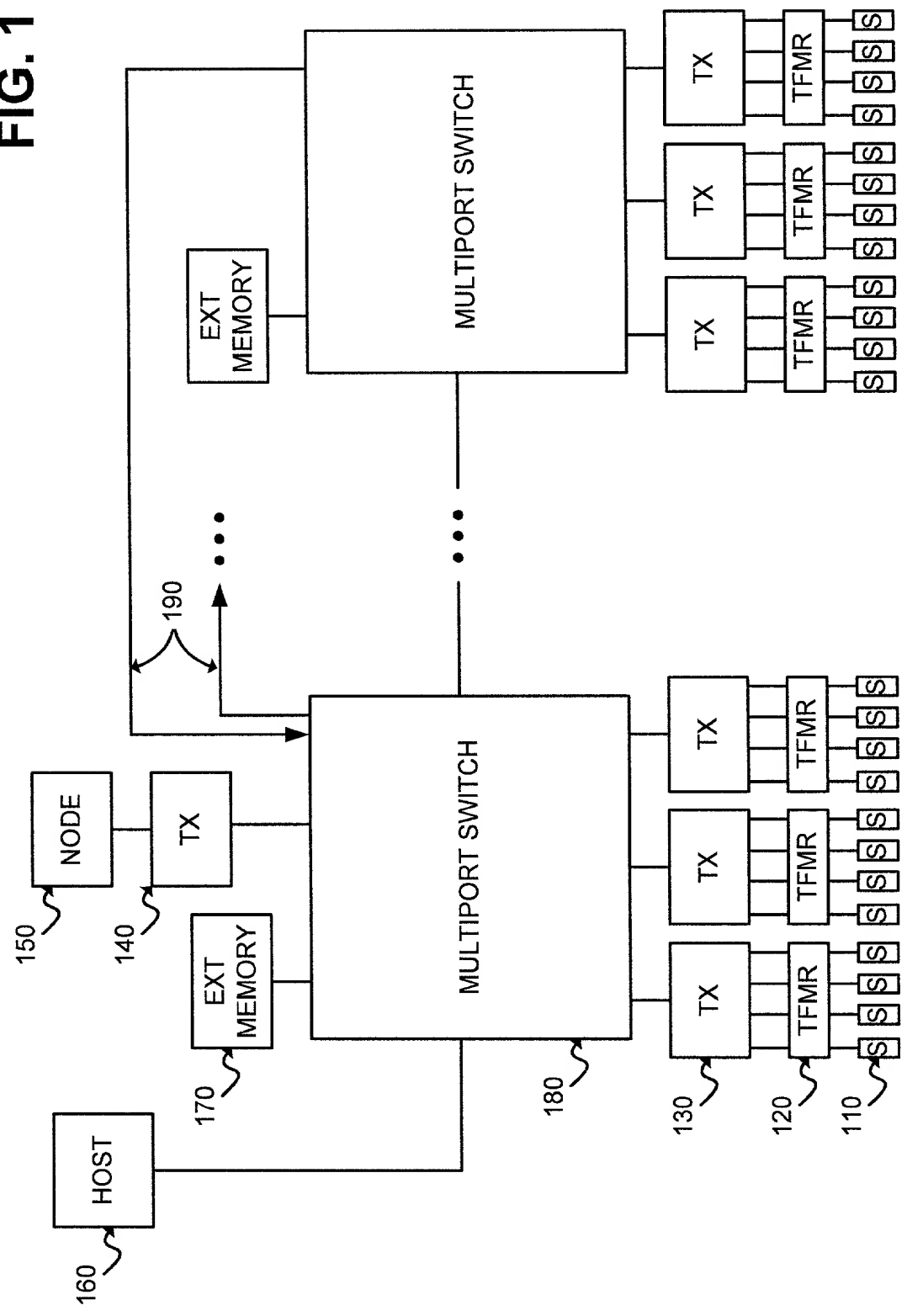
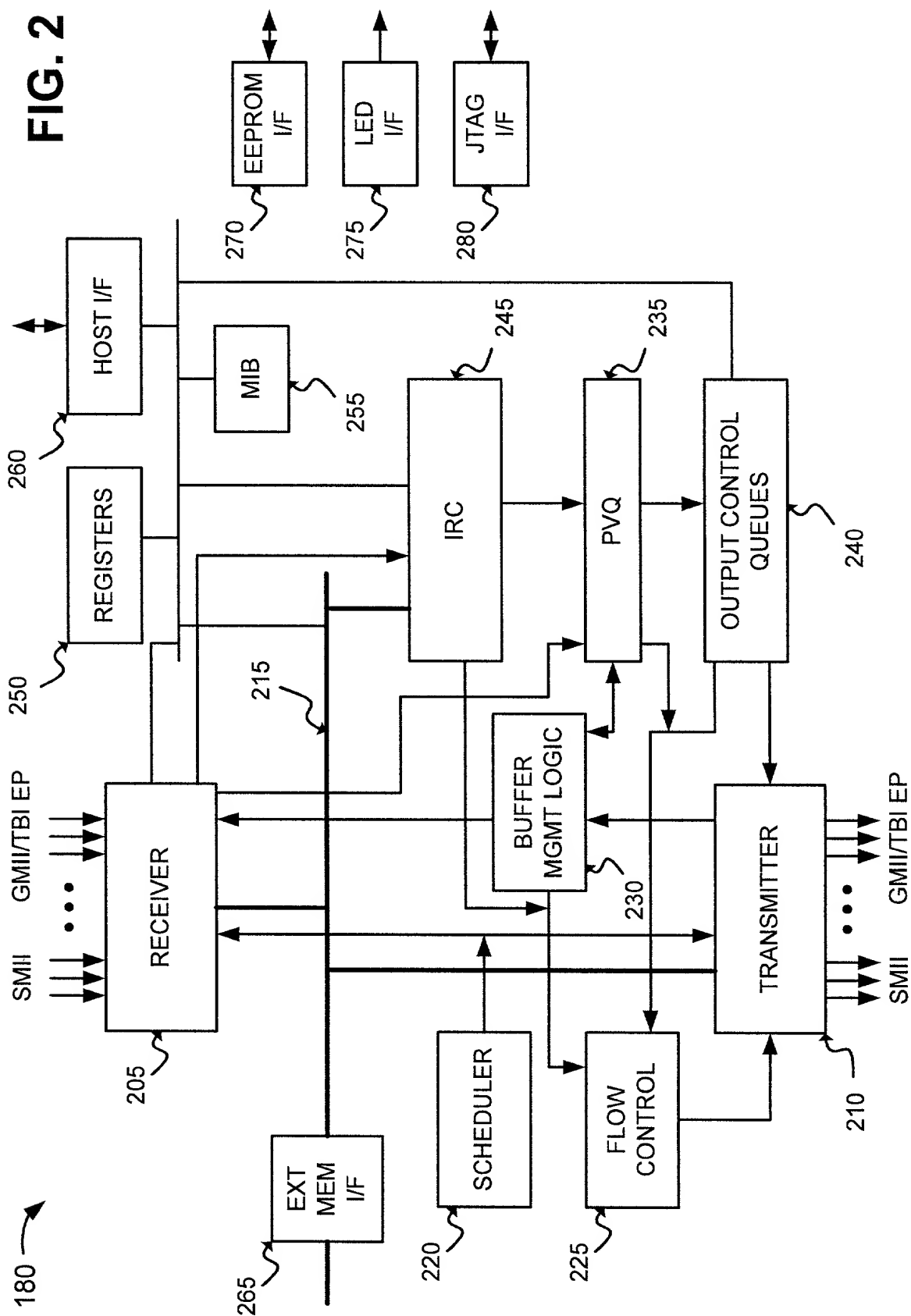
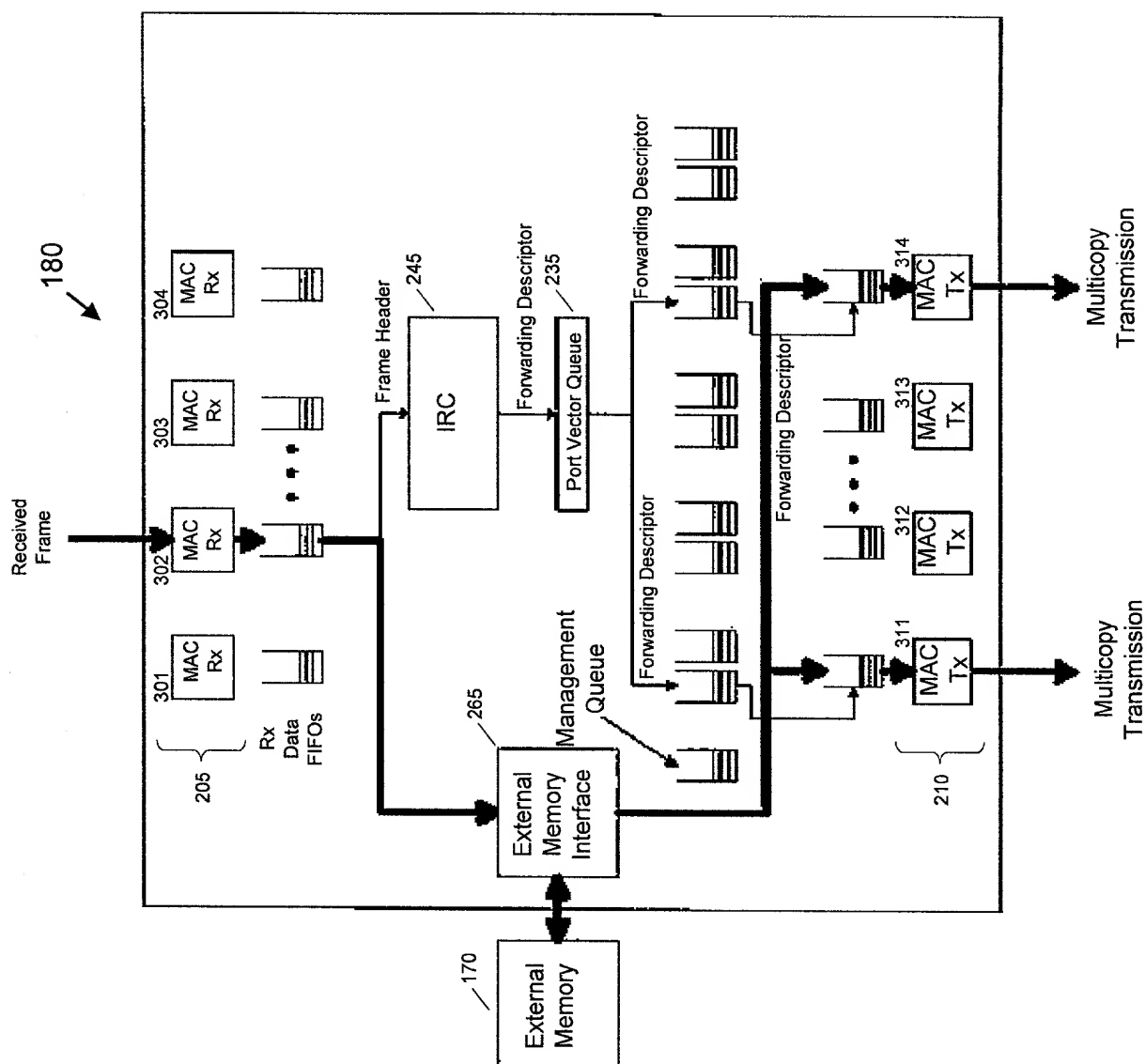


100

FIG. 1







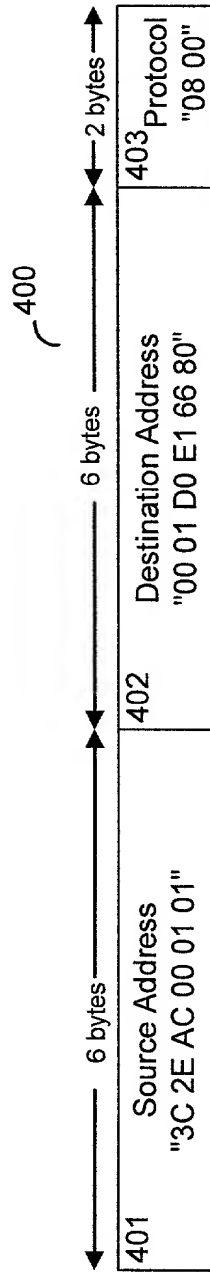


Fig. 4

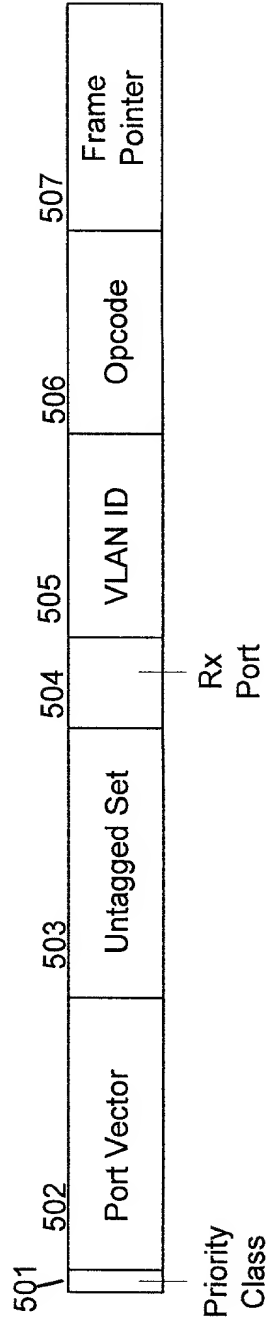


Fig. 5

FIG. 6 is a block diagram of a network device 245, according to one embodiment. The network device 245 includes an ingress filter 601, an SA lookup 602, a DA lookup 603, an egress filter 604, and an address table 605. The ingress filter 601 receives frame headers and outputs them to the SA lookup 602. The SA lookup 602 includes a SAL (Source Address List) and outputs to the DA lookup 603. The DA lookup 603 outputs to the egress filter 604, which outputs a frame forwarding descriptor to PVQ 235. The address table 605 is connected to both the SA lookup 602 and the DA lookup 603.

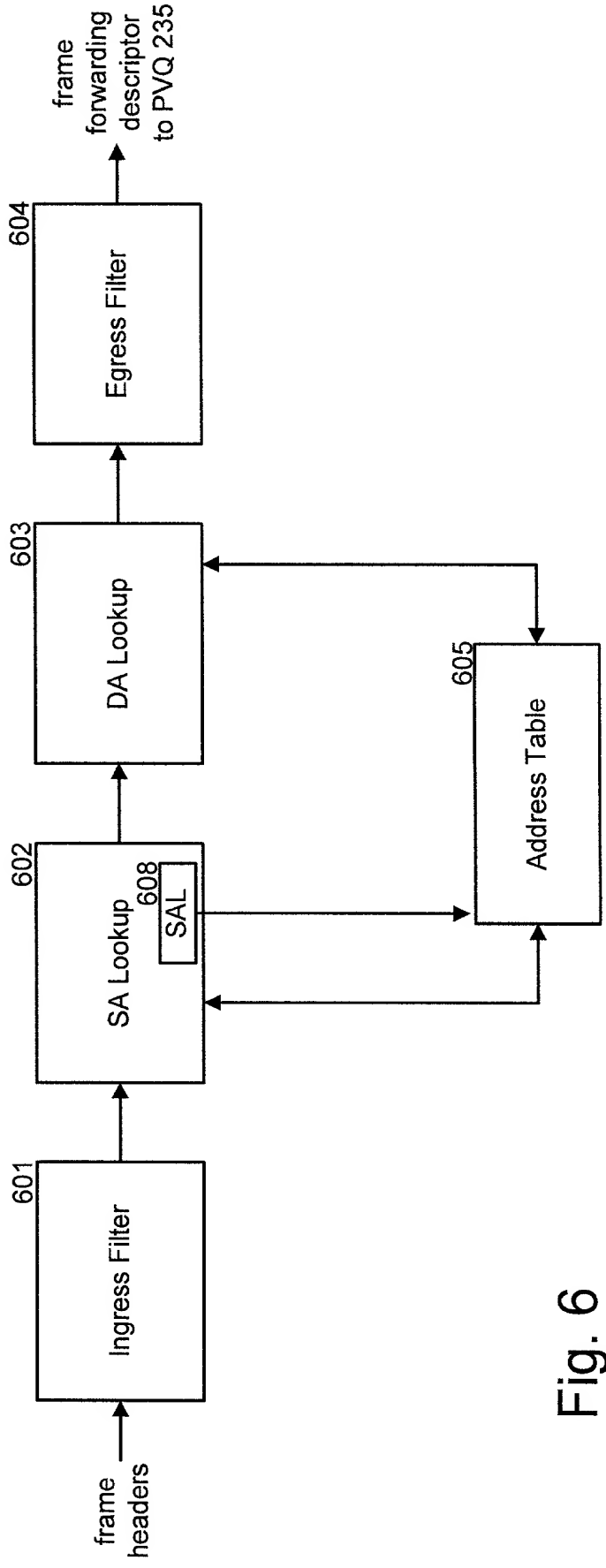


Fig. 6

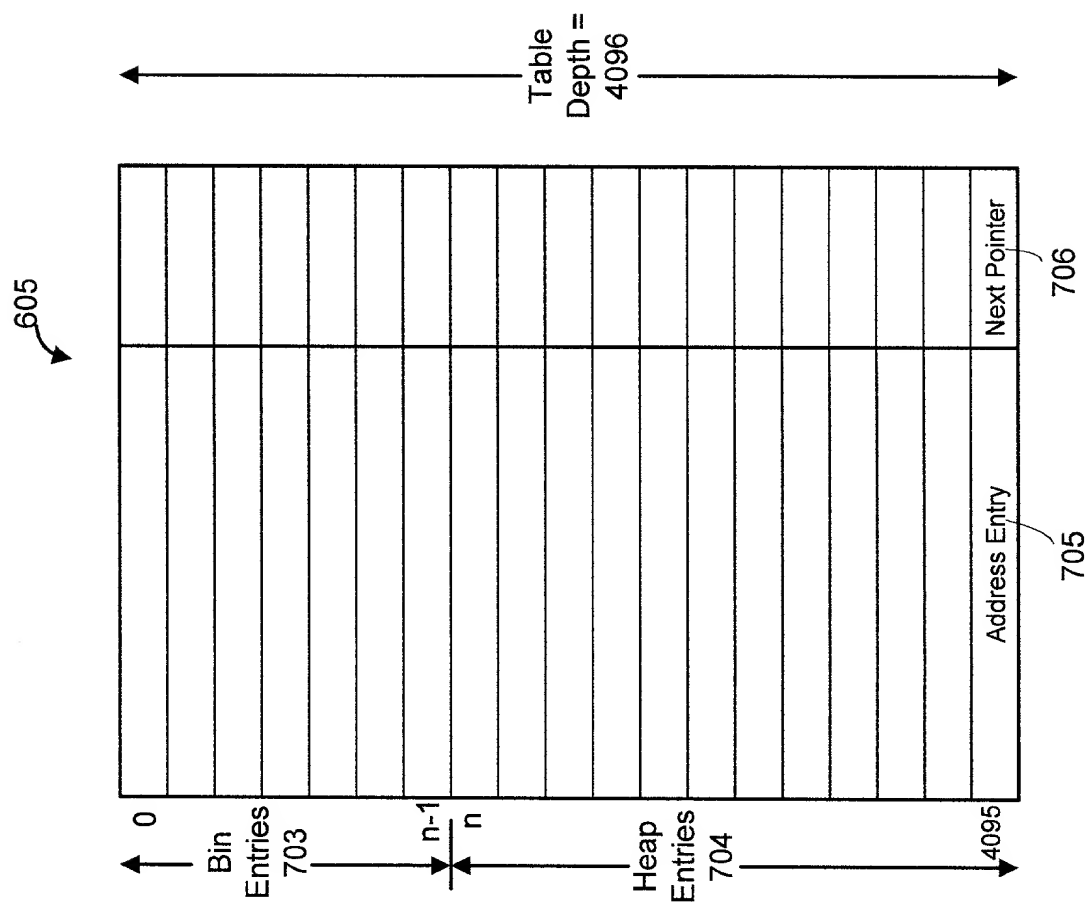


Fig. 7

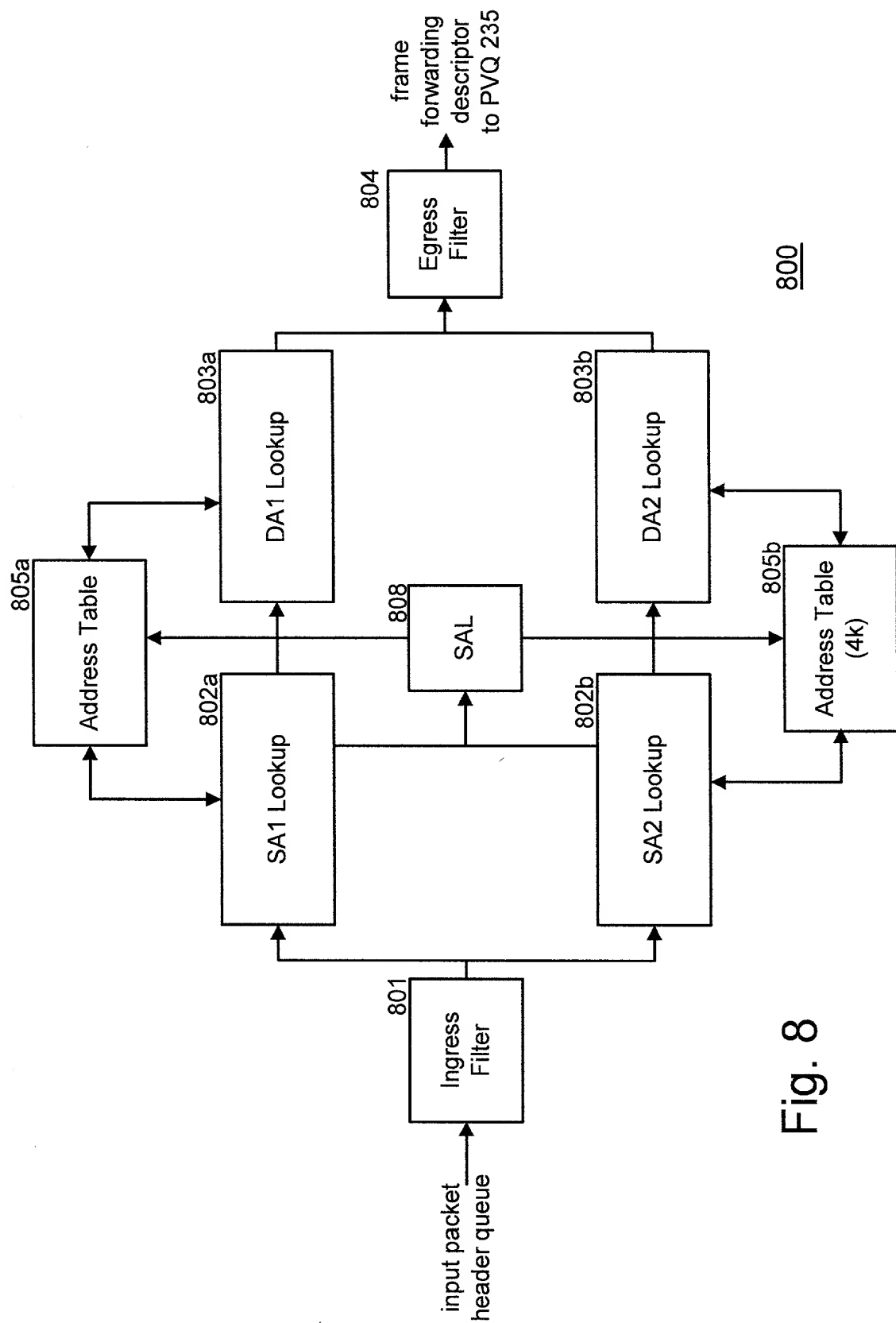


Fig. 8

FIG. 9 is a block diagram of a network device 900, according to one embodiment. The network device 900 includes an input packet header queue, an ingress filter 901, a source address lookup (SA) block 902a, a destination address lookup (DA) block 903a, a source address lookup (SA) block 902b, a destination address lookup (DA) block 903b, an address table 910, and an egress filter 904. The input packet header queue feeds into the ingress filter 901. The ingress filter 901 feeds into both the SA block 902a and the SA block 902b. The SA block 902a feeds into the DA block 903a. The SA block 902b feeds into the DA block 903b. The DA block 903a feeds into the egress filter 904. The DA block 903b feeds into the address table 910. The address table 910 feeds into both the SA block 902a and the SA block 902b. The egress filter 904 outputs a frame forwarding descriptor to PVQ 235.

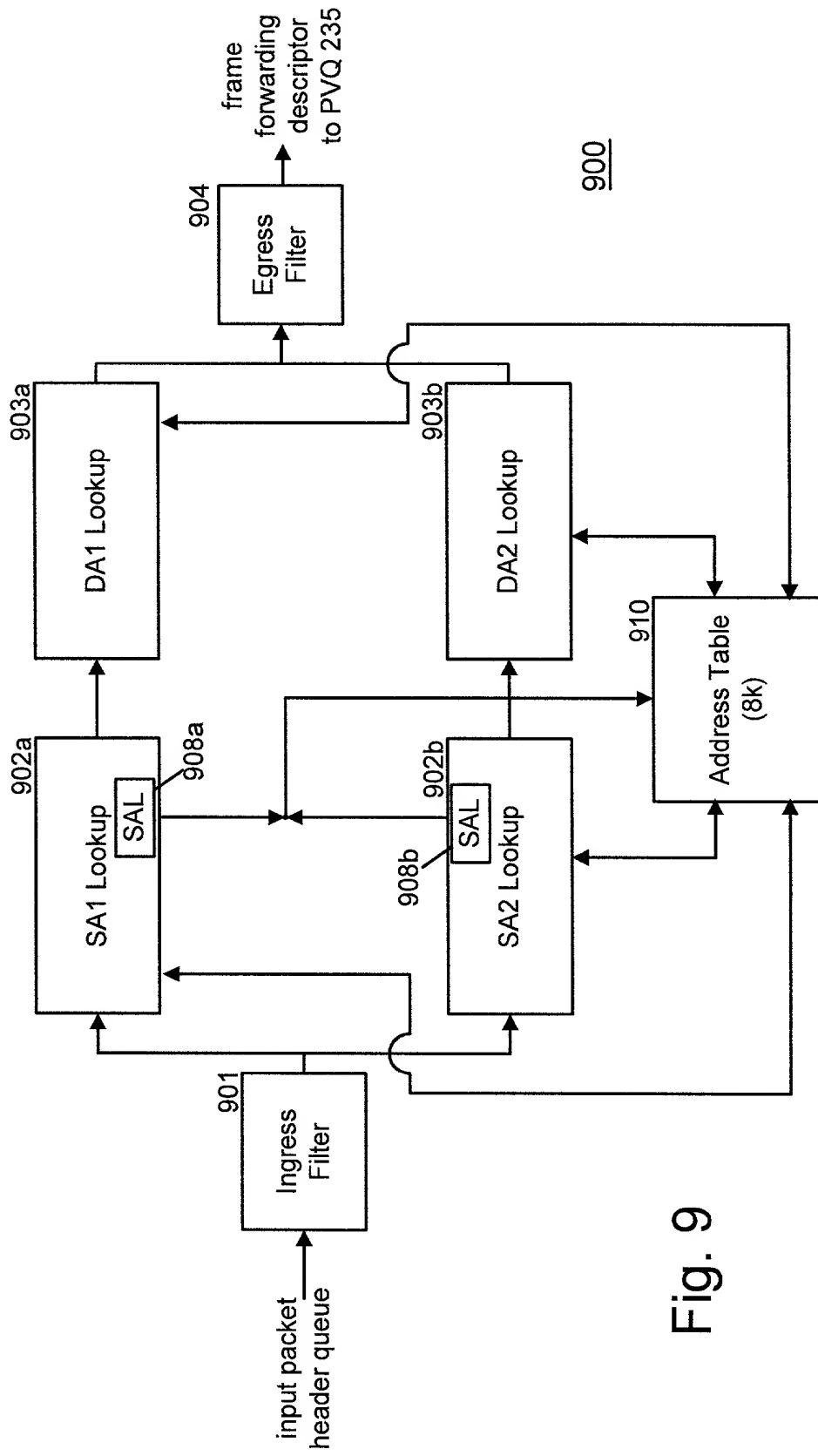


Fig. 9